

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	1282	self adj refresh\$3 near5 (memory or \$2ram)	US-PGPUB; USPAT	2004/11/13 17:59	
2	BRS	L2	4392	power\$3 adj2 down near5 (signal or circuit)	US-PGPUB; USPAT	2004/11/13 18:00	
3	BRS	L3	141	1 and 2	US-PGPUB; USPAT	2004/11/13 18:00	
4	BRS	L4	576320	(processor or microprocessor or cpu or mpu)	US-PGPUB; USPAT	2004/11/13 18:01	
5	BRS	L5	509	sleep adj2 (command or instruction)	US-PGPUB; USPAT	2004/11/13 18:02	
6	BRS	L6	168	4 same 5	US-PGPUB; USPAT	2004/11/13 18:02	
7	BRS	L7	2	3 and 6	US-PGPUB; USPAT	2004/11/13 18:19	
8	BRS	L8	74	3 and 4	US-PGPUB; USPAT	2004/11/13 18:19	
9	BRS	L9	131176	power adj3 control\$3	US-PGPUB; USPAT	2004/11/13 18:20	
10	BRS	L10	29	8 and 9	US-PGPUB; USPAT	2004/11/13 19:23	
11	BRS	L11	2	3 and 5	US-PGPUB; USPAT	2004/11/13 19:24	
12	BRS	L12	126552	memory adj3 control\$3	US-PGPUB; USPAT	2004/11/13 19:25	
13	BRS	L13	41008	clock adj3 control\$3	US-PGPUB; USPAT	2004/11/13 19:25	
14	BRS	L14	2135	9 and 12 and 13	US-PGPUB; USPAT	2004/11/13 19:25	
15	BRS	L15	25	3 and 14	US-PGPUB; USPAT	2004/11/13 19:25	
16	BRS	L16	12	15 not 10	US-PGPUB; USPAT	2004/11/13 19:48	
17	BRS	L17	2401	713/300.ccls. 713/320.ccls. 713/322.ccls.	US-PGPUB; USPAT	2004/11/13 19:49	
18	BRS	L18	1502	713/324.ccls. 713/600.ccls. 713/601.ccls.	US-PGPUB; USPAT	2004/11/13 19:50	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
19	BRS	L19	3538	17 or 18	US- PGPUB; USPAT	2004/11/13 19:51	
20	BRS	L20	12	3 and 19	US- PGPUB; USPAT	2004/11/13 19:51	